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**INFORMATION DISCLOSURE STATEMENT  
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Applicant: INAGAKI, Yasushi et al

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**U.S. PATENT DOCUMENTS**

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
	AR					
	BR					
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	DR					
	ER					
	FR					

**FOREIGN PATENT DOCUMENTS**

	Document Number	Date MM/YYYY	Country	Inventor Name	English Abstract	Translation Readily Available
	GR				Enclosed	No
	HR				Enclose	No
	IR					
	JR					
	KR					
	LR					
	MR					
	NR					
	OR					
	PR					

**OTHER** (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

10	QR	PATENT ABSTRACTS OF JAPAN, "MULTILAYERED WIRING BOARD" JP 11-126978, May 11, 1999, Akiya.Fujisaki et al
1	RR	PATENT ABSTRACTS OF JAPAN " METHOD FOR MANUFACTURING MULTILAYER PRINTED WIRING BOARD ", JP 10-013024, January 1, 1998, Satoshi Nakamura
1	SR	PATENT ABSTRACTS OF JAPAN, "ELECTRODE STRUCTURE AND THE MANUFACTURING METHOD", JP 54-157296, December 12, 1979
10	TR	PATENT ABSTRACTS OF JAPAN, " MULTILAYER ELECTRONIC PART AND MANUFACTURE THEREOF ", JP 8-241827, September 17, 1996
1	UR	
1	VR	

Examiner TUAN DINH

Date Considered: 12/27/02

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.